

## WEST Search History





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	<i>DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR</i>		
<input type="checkbox"/>	L17	L14 and L13	0
<input type="checkbox"/>	L16	L15 and L13	0
<input type="checkbox"/>	L15	L14 and L11	2
<input type="checkbox"/>	L14	L5 and L9	21
<input type="checkbox"/>	L13	(multi-layer) near (security protocol) with (mobile) adj (phone or device)	1
<input type="checkbox"/>	L12	(multi-layer) near (security protocol) with (memory) near (high performance)	0
<input type="checkbox"/>	L11	L10 and "API"	2
<input type="checkbox"/>	L10	L9 and L5	21
<input type="checkbox"/>	L9	(memory) near (high performance)	23052
<input type="checkbox"/>	L8	L5 and L4	2
<input type="checkbox"/>	L7	L5 and L3	4
<input type="checkbox"/>	L6	L5 and L2	11
<input type="checkbox"/>	L5	(mobile) adj (phone or device) near (processor or digital signal processor)	4474
<input type="checkbox"/>	L4	713/151.ccls.	407
<input type="checkbox"/>	L3	380/247.ccls.	298
<input type="checkbox"/>	L2	380/270.ccls.	759
	<i>DB=PGPB,USPT; PLUR=YES; OP=OR</i>		
<input type="checkbox"/>	L1	20020112167 6088800.pn.	2

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mobile device, high performance processor, di

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**Web** Results 1 - 2 of 2 for **mobile device, high performance processor, digital signal processor, command**

Tip: Try removing quotes from your search to get more results.

**Method and system to accelerate cryptographic functions for secure ...**

4. The **device** of claim 1 wherein the **high performance processor** further comprises a **digital signal processor**. 5. The **device** of claim 4 wherein the **digital ...**

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